REMARKS

Claim 26 is amended to provide formatting and punctuation.

Claim 40 is amended to provide formatting and to correct two typographical errors: changing "variable" to "variabl**y**," and adding the article "a" to the phrase "**a** plurality of separate system units...."

Claim 53 is amended to provide formatting only. No change to the text of the claim is being made.

Claim 61 is amended to correct a typographical error: the word "comprising" should read "compris**es**."

Entry of the amendments is respectfully requested. Applicant particularly requests the Examiner preserve the formatting (tabbing, spacing) of these amendments upon their entry into the record, it will improve the clarity and readability of the claims.

CONCLUSION

The Commissioner is authorized to charge any fees that may be required, or credit any overpayment, to Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C. Deposit Account No. 501505/5181-92401/DMM.

Respectfully submitted,

Date: November 24, 2009

By: /Dean M. Munyon/
Dean M. Munyon
Reg. No. 42,914

Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C.
P. O. Box 398
Austin, Texas 78767
(512) 853-8847

APPENDIX

This partial Appendix is provided for the convenience of the Examiner. Claims 40 and 61 are shown relative to their previous version. The claims below are not to be entered into the record and are shown for informational purposes.

40. (Thrice Amended) A multiprocessor computer having hardware domains **variably[[e]]** configurable by commands from an operator, said computer comprising:

a plurality of separate system units for performing sequences of transactions, each including at least one of:

a processor unit for generating addresses within a predetermined global range,

a memory unit for storing data at a set of addresses within said predetermined global range, and

an input/output adapter for generating and/or receiving a set of addresses within said predetermined global range;

a global address router coupled to said system units for transferring addresses generated in any of said system units to others of said system units;

a global data router for transferring data from any of said system units to others of said system units;

a control-signal distributor for communicating a plurality of control signals from any of said system units to others of said system units for affecting the operation of all of said system units in response to conditions occurring in said any system unit;

a domain configurator for electronically dividing said computer into a plurality of software-configurable hardware domains each comprising an arbitrary subset of said system units independently of any physical reconnection of said system units within said computer; and

a domain filter coupled to all of said system units for electronically inhibiting at least some of said control signals originating in those of said system units within one of said domains from affecting certain of said system units outside said one domain, wherein said domain filter is coupled to at least one of said global routers for inhibiting transactions on said one global router

originating in those of said system units within one of said domains from being received in certain of said system units outside said one domain.

61. (Once Amended) The computer system according to claim 60, wherein said domain filter further **comprise<u>s</u>[[ing]]:**

a plurality of cluster registers each identifying to which cluster each of said plurality of system units belongs, wherein said plurality of cluster registers is responsive to a current one of said transactions;

a second connection for transmitting a valid-transaction signal to each of said plurality of system units belonging to a given cluster for any of said transactions originating from one of said plurality of system units belonging to said given cluster.